

FIG. 1

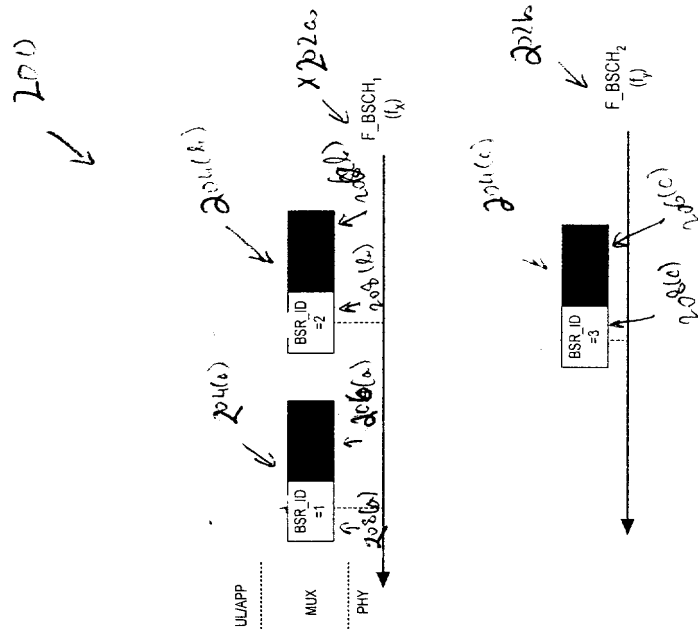


FIG. 2

Reed-Soloman Outer Code Block Diagram

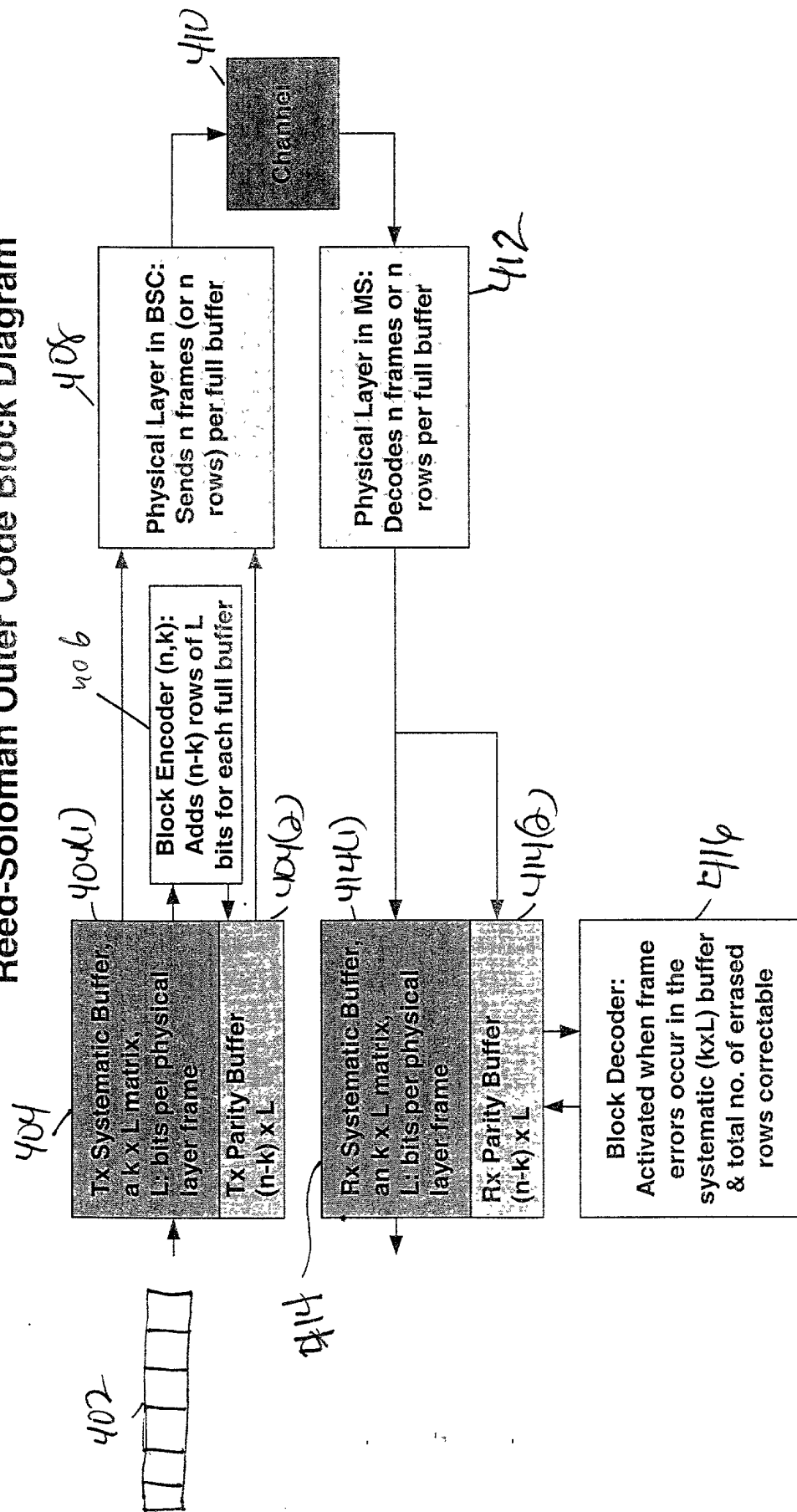


FIG. 4

Tx Buffer for Outer Code

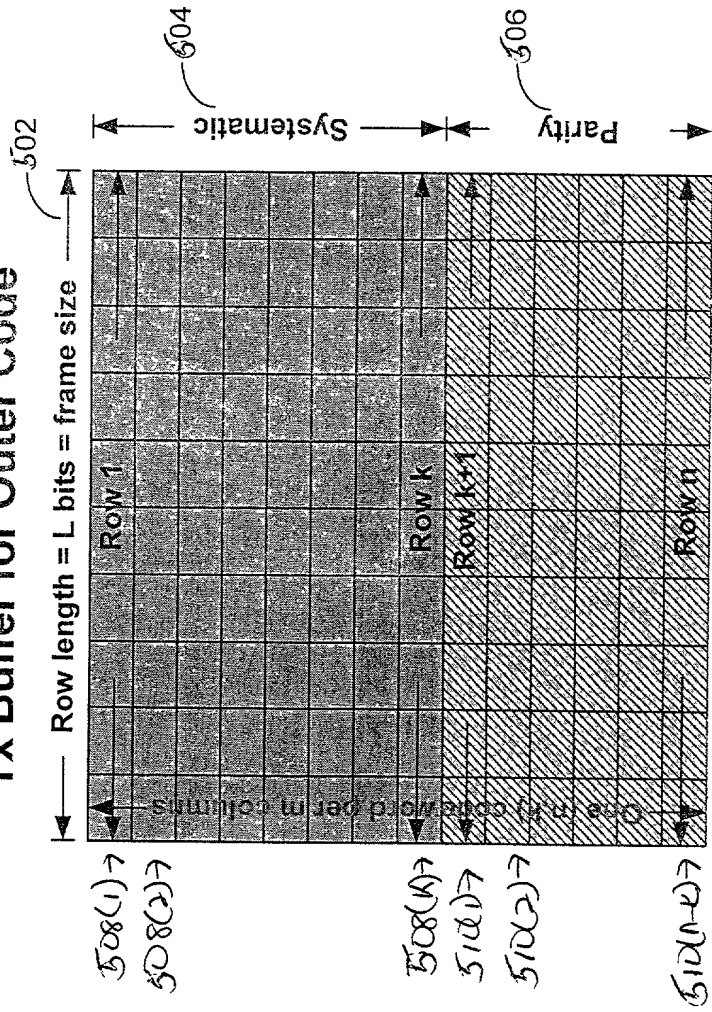


FIG. 5

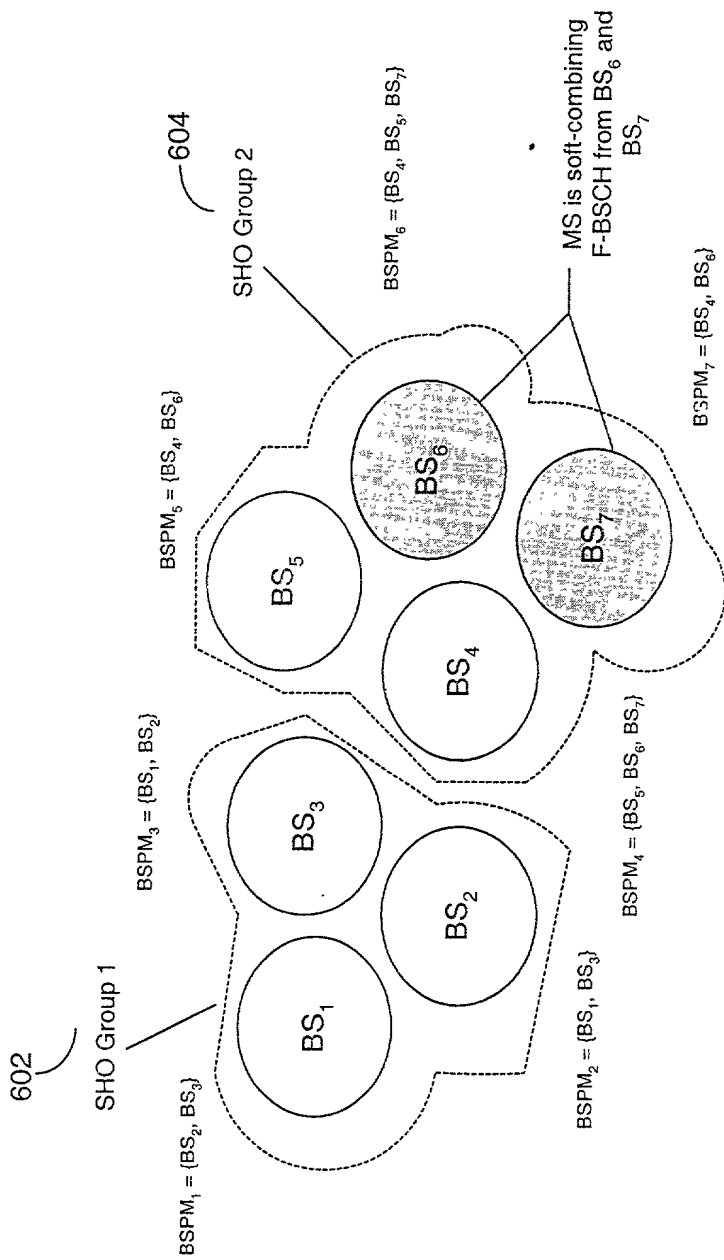


FIG. 6

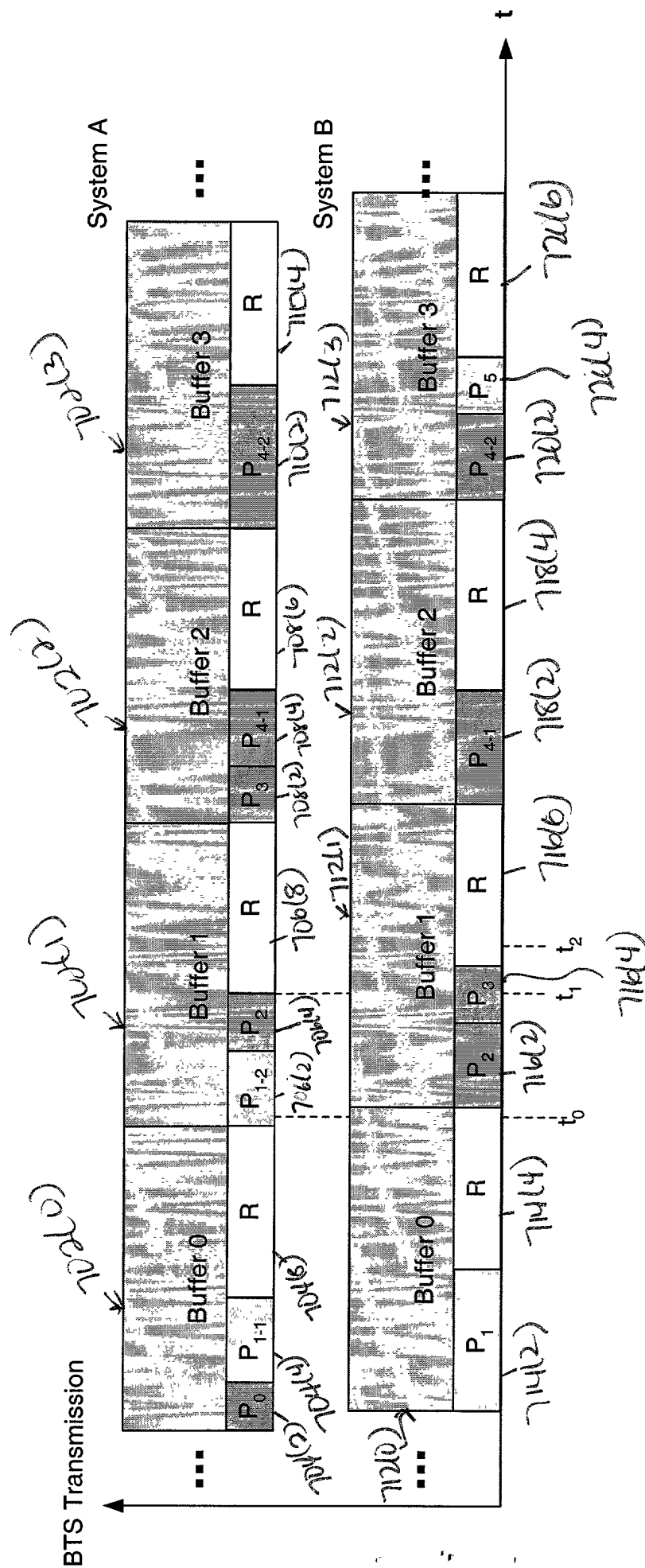


FIG. 7